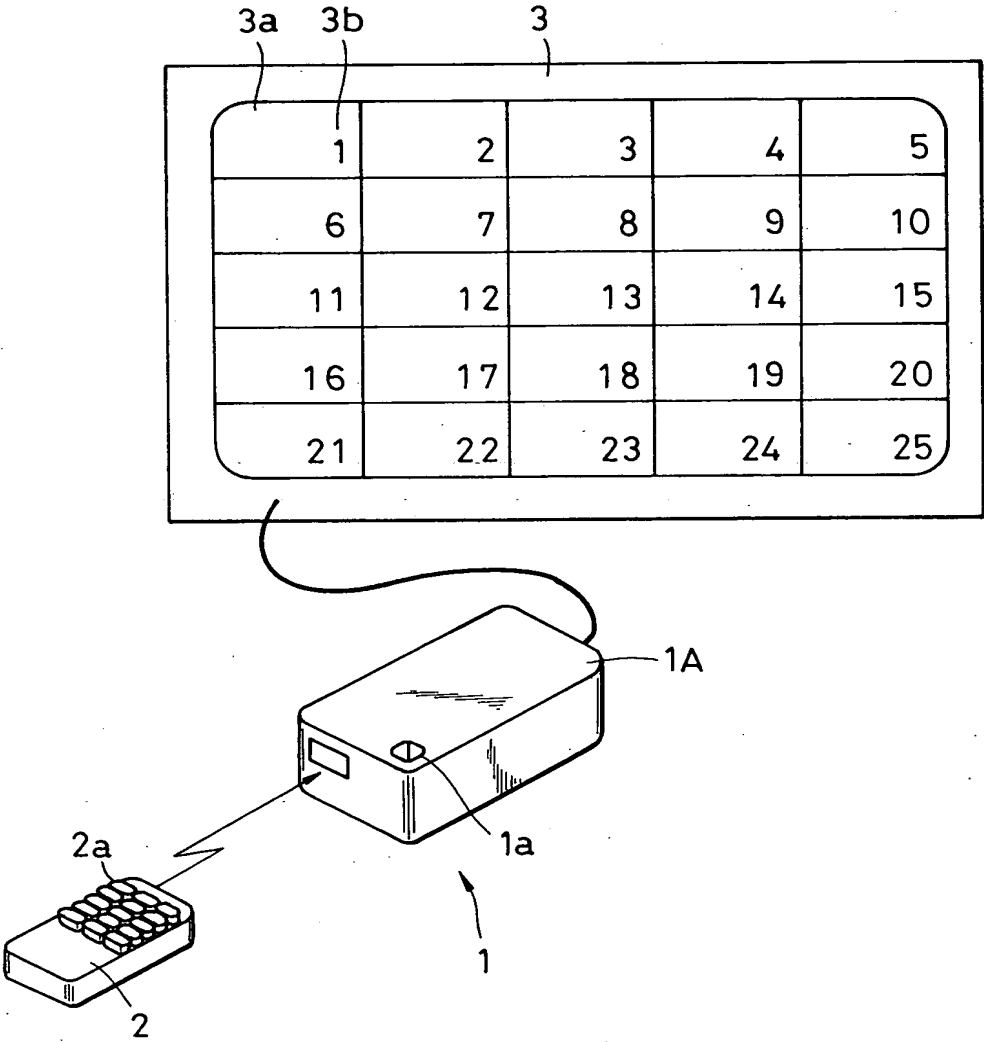


FIG. 1



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 3

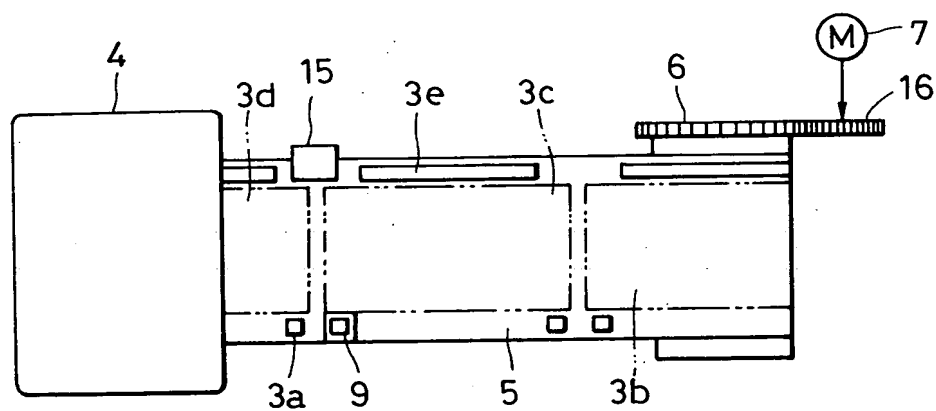


FIG. 4

1A

PHOTO-REFLECTOR 9

MAGNETIC HEAD 15

140

MICROCOMPUTER

144

MAGNETICALLY-RECORDED-INFORMATION REPRODUCTION CIRCUIT

143

MOTOR DRIVING CIRCUIT

7

MOTOR

8

DETECTING MEMBER

11

LIGHT-SOURCE DRIVING CIRCUIT

10

LIGHT SOURCE

141 (SIGNAL PROCESSING CIRCUIT)

142 (MEMORY)

142-1

142-2

142-3

MEMORY 142-1

MEMORY 142-2

MEMORY 142-3

141-8

MEMORY CONTROL UNIT 141-8

141-9

BUS CONTROL CIRCUIT 141-9

141-10

MEMORY INTERFACE 141-10

141-81

141-82

141-83

ADDRESS CONTROL CIRCUIT 141-81

ADDRESS CONTROL CIRCUIT 141-82

ADDRESS CONTROL CIRCUIT 141-83

141-4

VIDEO-SIGNAL GENERATING CIRCUIT 141-4

141-5

141-6

141-7

VIDEO AMPLIFIER

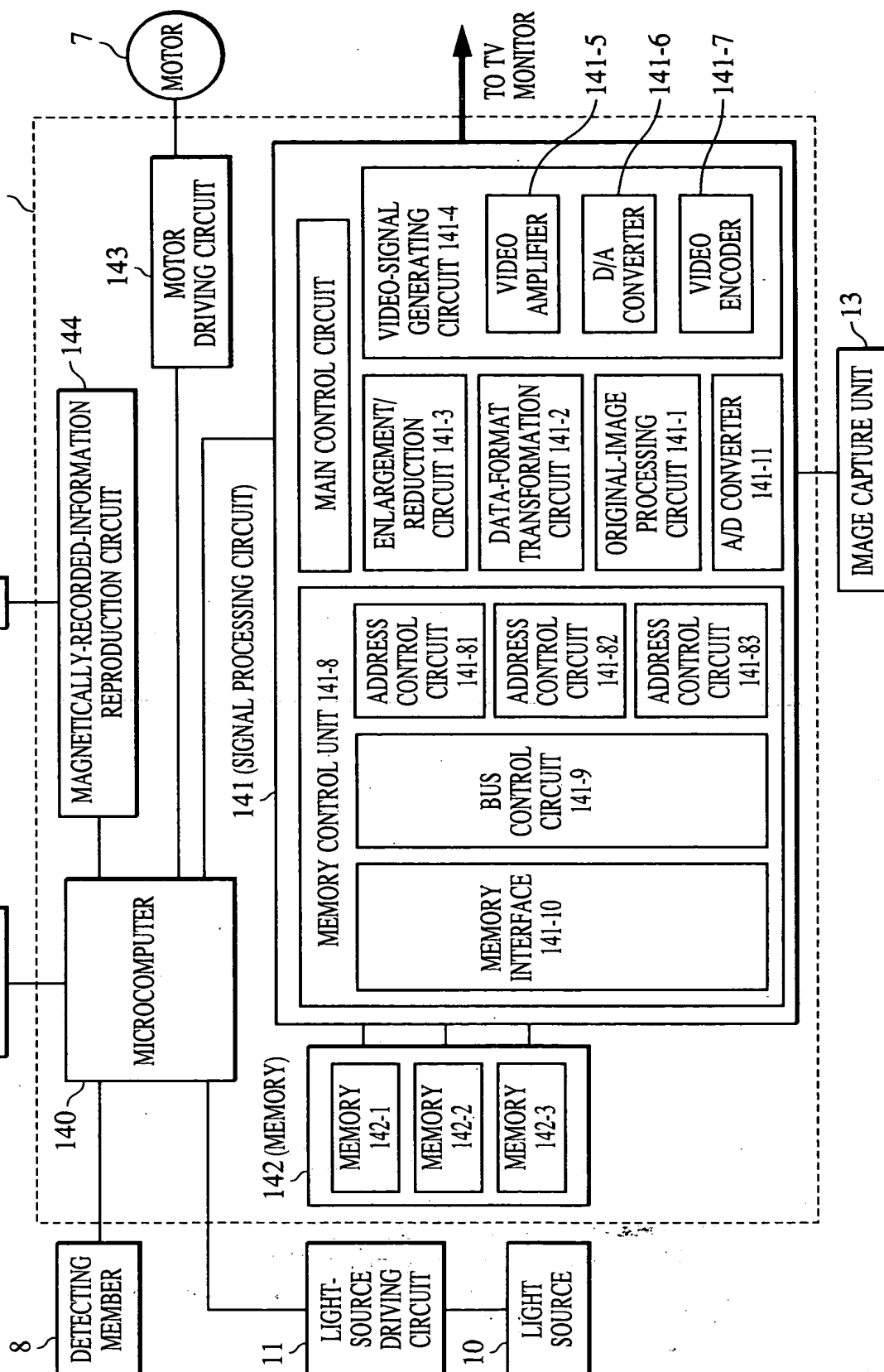
D/A CONVERTER

VIDEO ENCODER

TO TV MONITOR

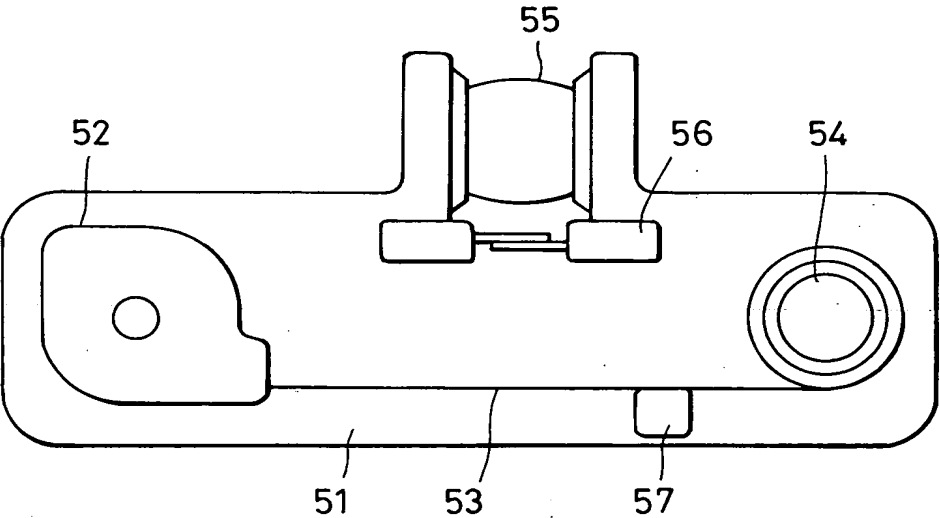
13

IMAGE CAPTURE UNIT



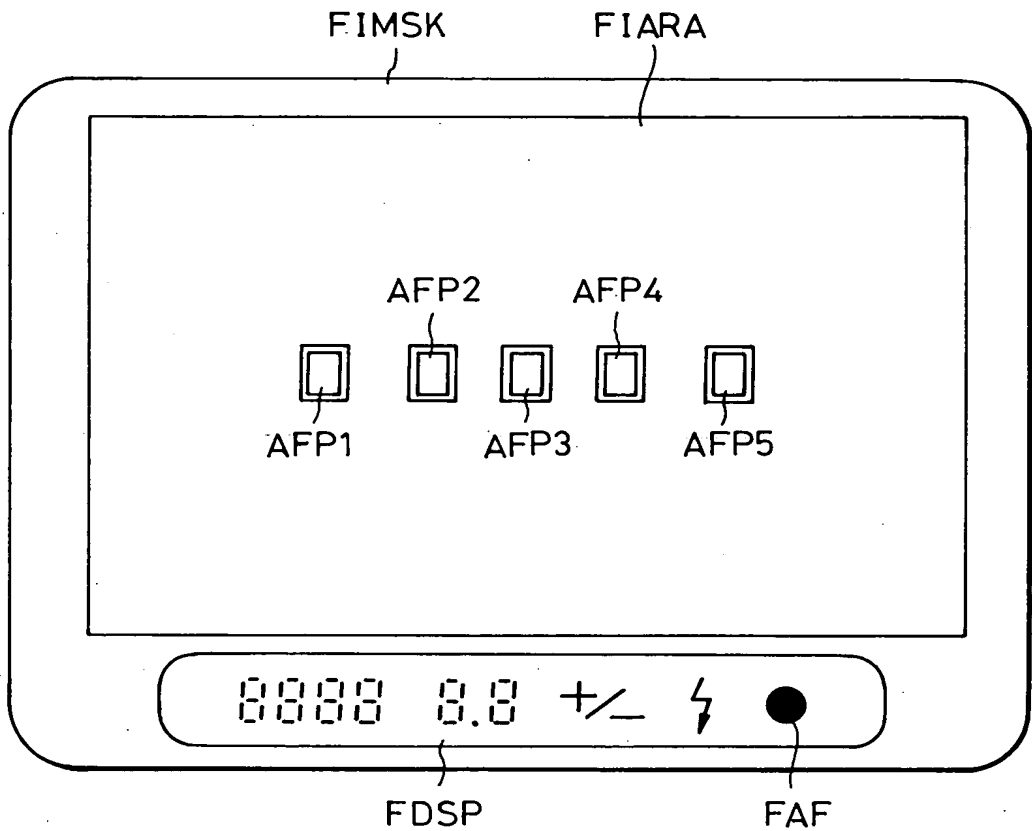
APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 5



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 6



00000-44502200

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 7A

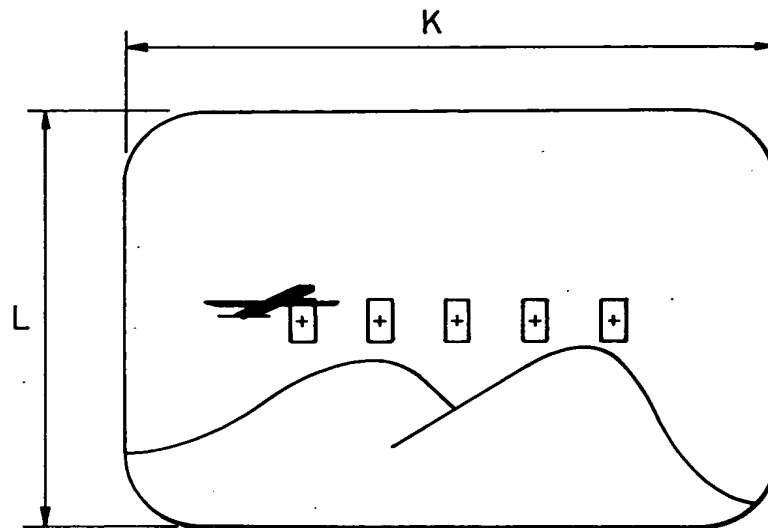
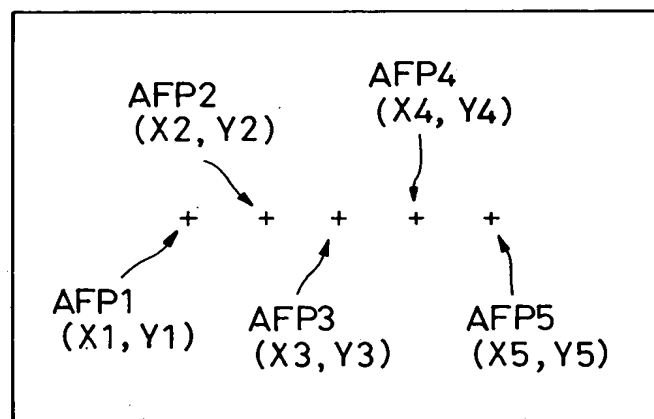


FIG. 7B

(0,0)



(K, L)

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 8

CAMERA ID	ARRANGEMENT OF FOCUS DETECTION POINTS
1	TYPE A
2	TYPE B
3	TYPE C
4	TYPE B
5	TYPE C

FIG. 9

FOCUS DETECTION ID	FOCUS DETECTION POINT
1	AFP 1
2	AFP 2
3	AFP 3
4	AFP 4
5	AFP 5

663720-44502260

FIG.10

ARRANGEMENTS OF FOCUS DETECTION POINTS	COORDINATES OF FOCUS DETECTION POINTS (FOCUS DETECTION ID)				
	AFP 1	AFP 2	AFP 3	AFP 4	AFP 5
TYPE A	X _A , Y _A	NO COORDINATES SET	NO COORDINATES SET	NO COORDINATES SET	NO COORDINATES SET
TYPE B	X _{B1} , Y _{B1}	X _{B2} , Y _{B2}	X _{B3} , Y _{B3}	NO COORDINATES SET	NO COORDINATES SET
TYPE C	X _{C1} , Y _{C1}	X _{C2} , Y _{C2}	X _{C3} , Y _{C3}	X _{C4} , Y _{C4}	X _{C5} , Y _{C5}
TYPE D	X _{D1} , Y _{D1}	X _{D2} , Y _{D2}	X _{D3} , Y _{D3}	NO COORDINATES SET	NO COORDINATES SET

FIG. 11

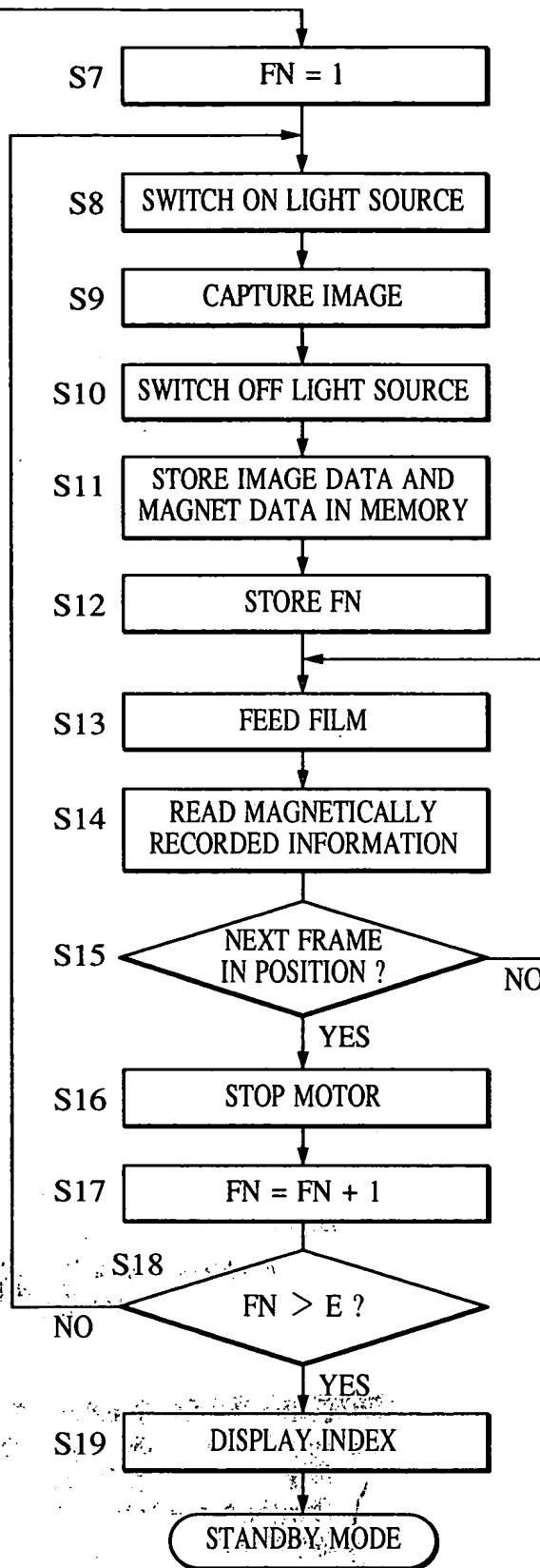
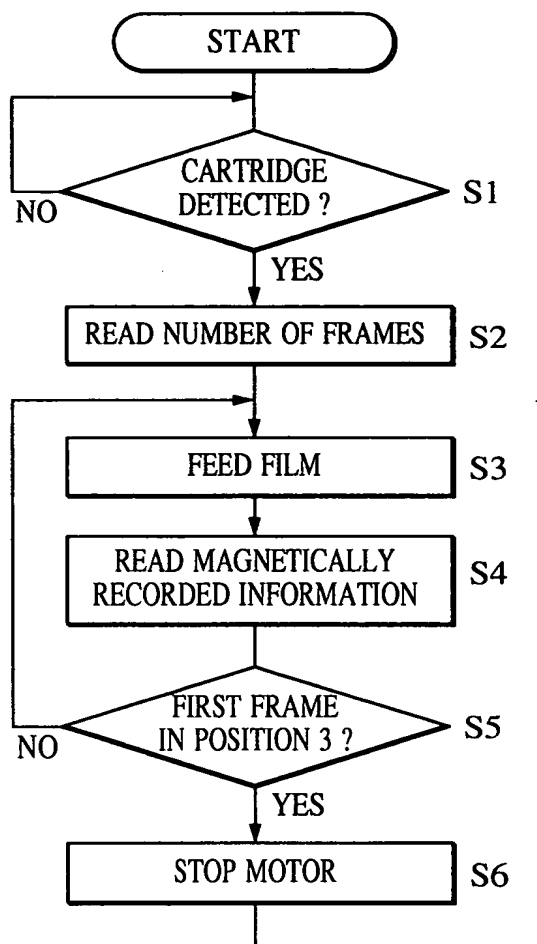


FIG. 12

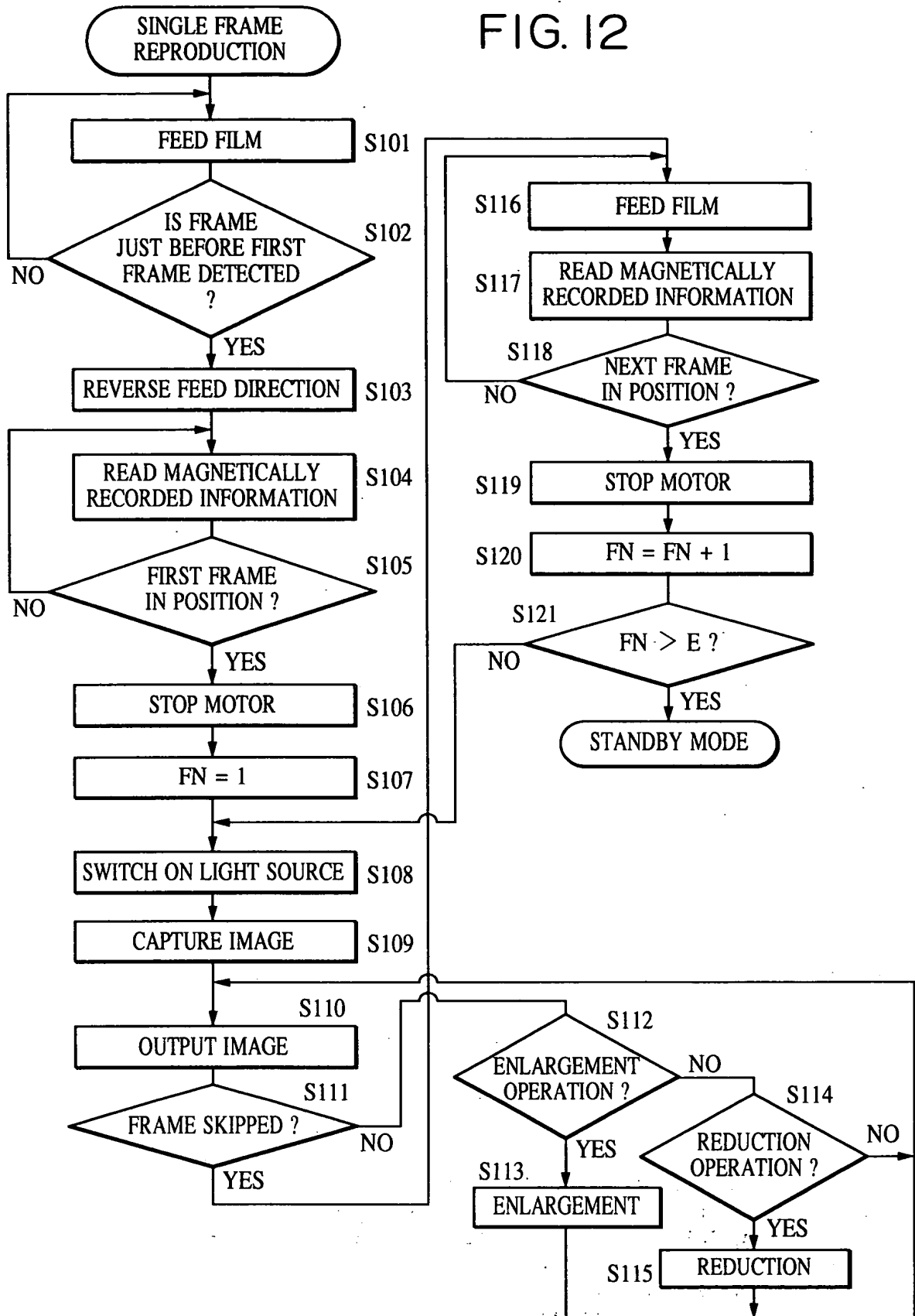


FIG. 13

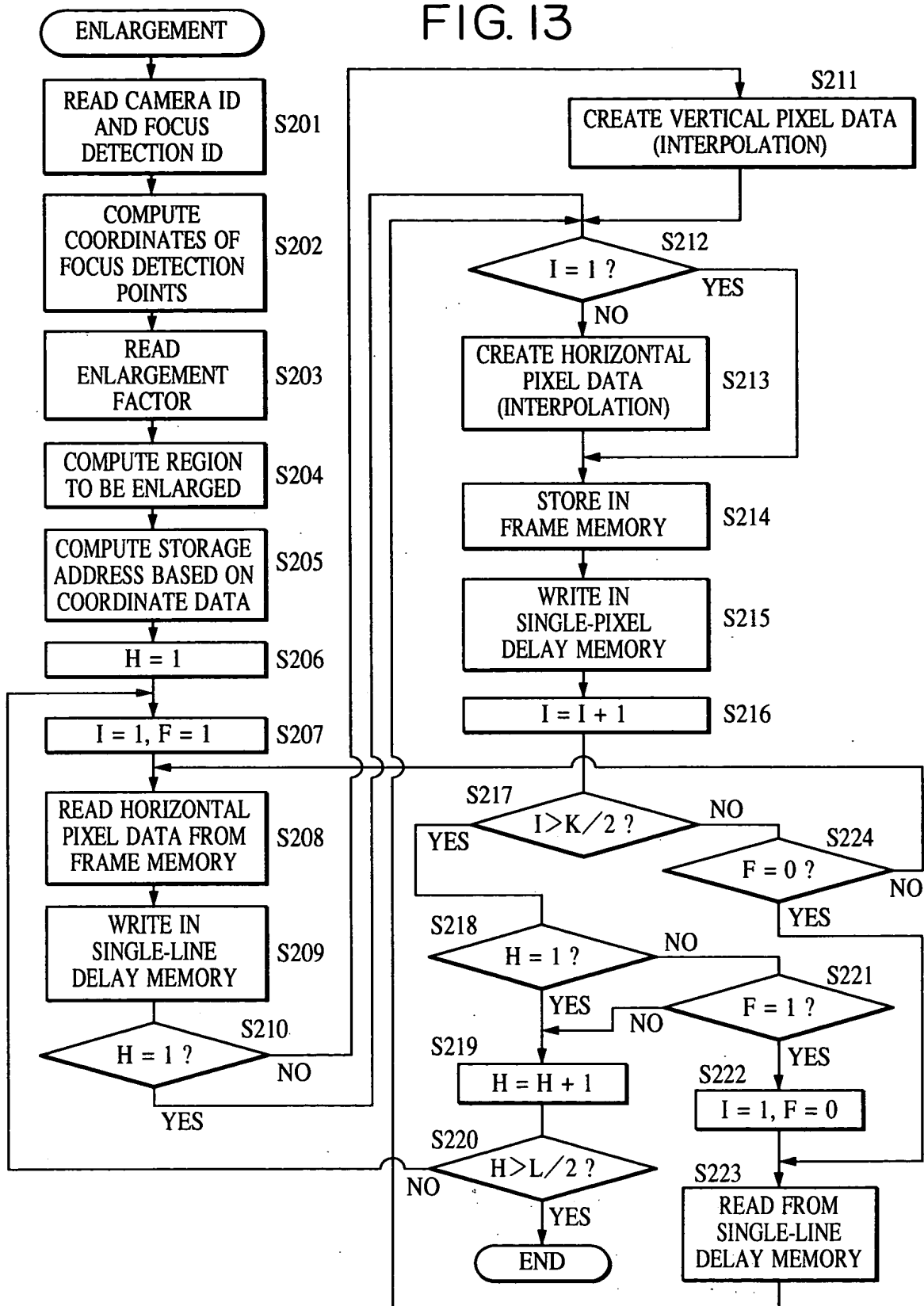


FIG. 15

141 SIGNAL PROCESSING CIRCUIT

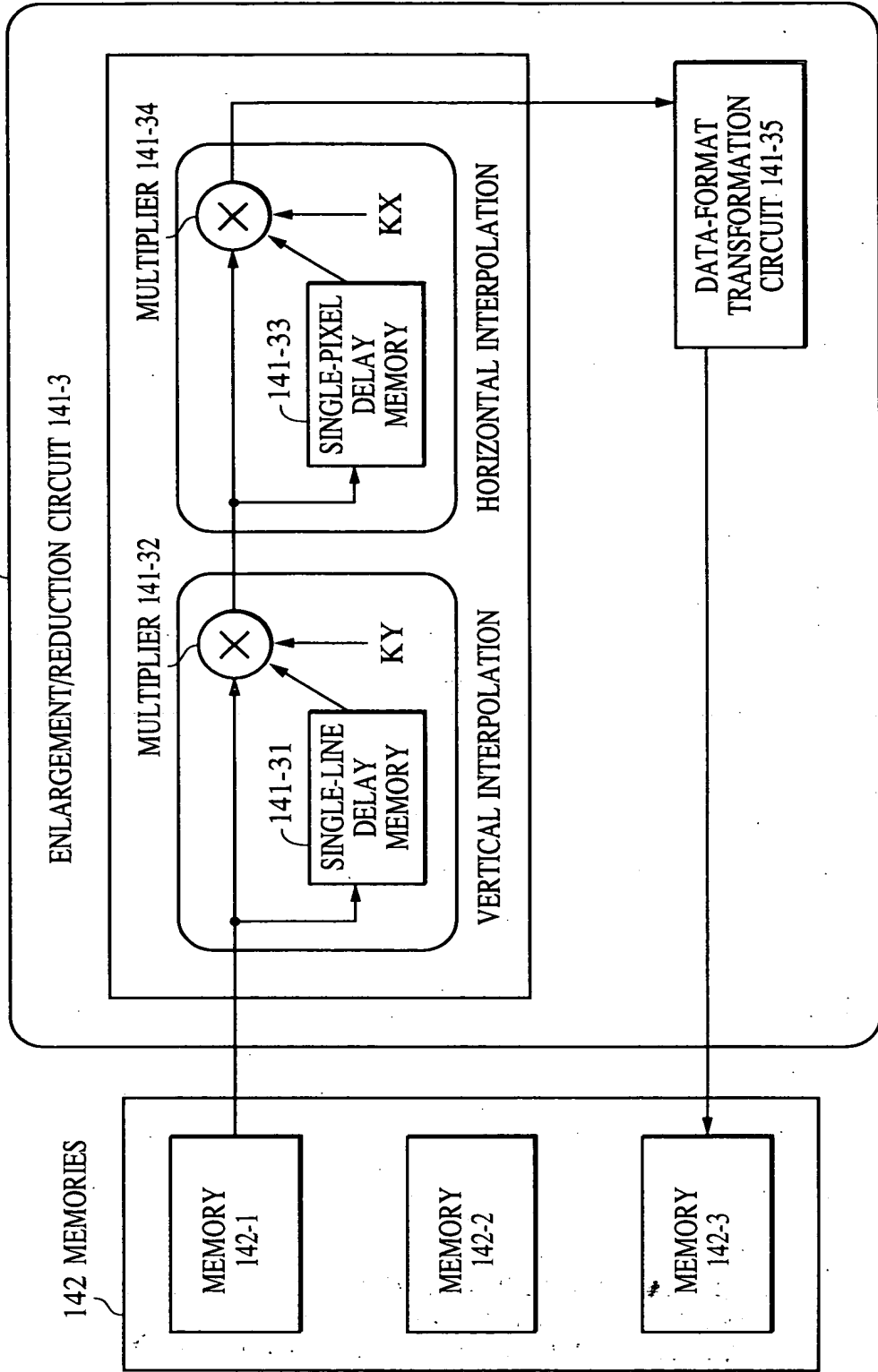
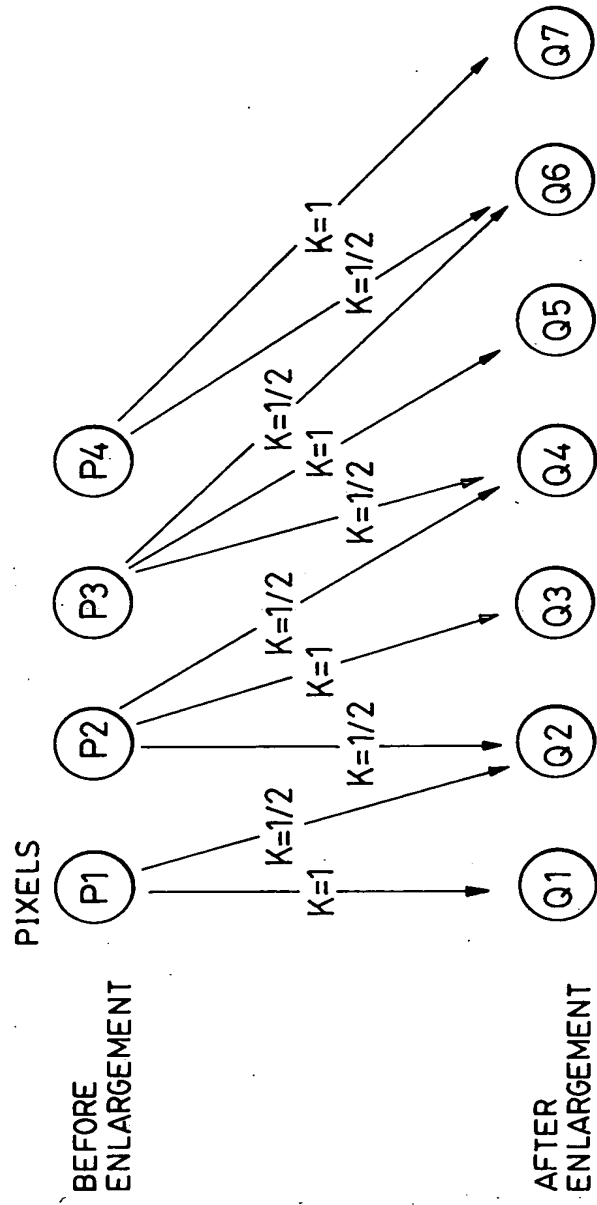
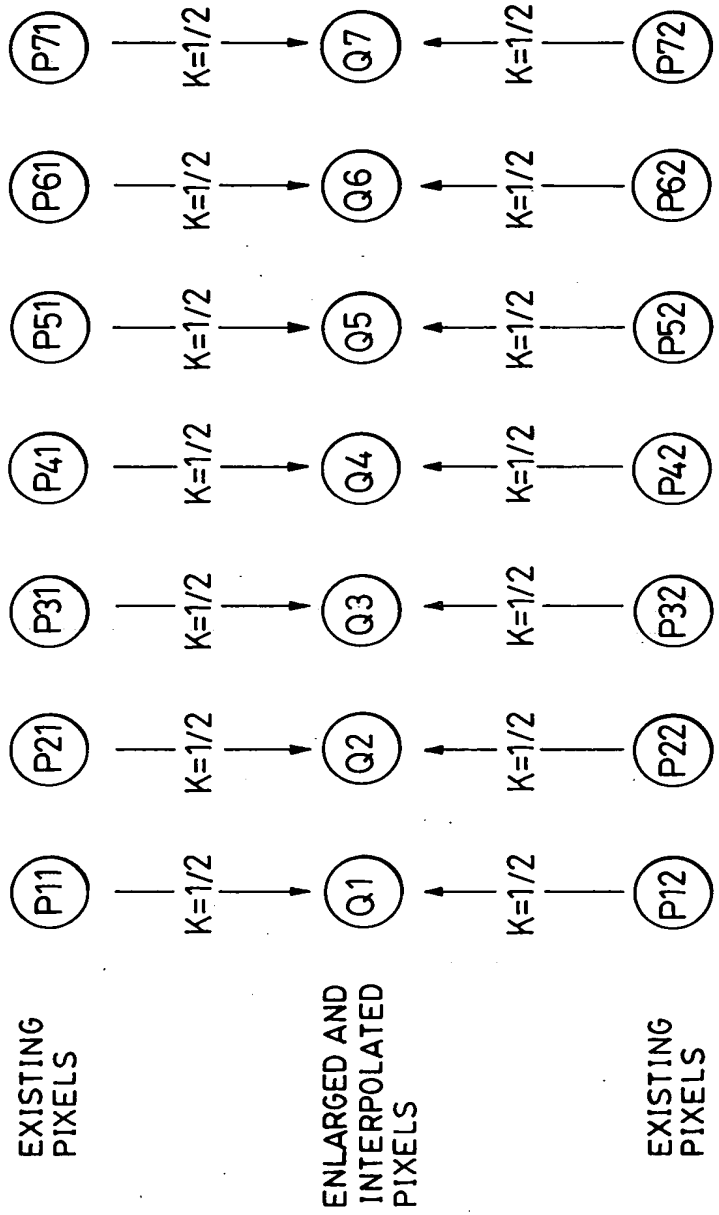


FIG. 16



62342-1136220

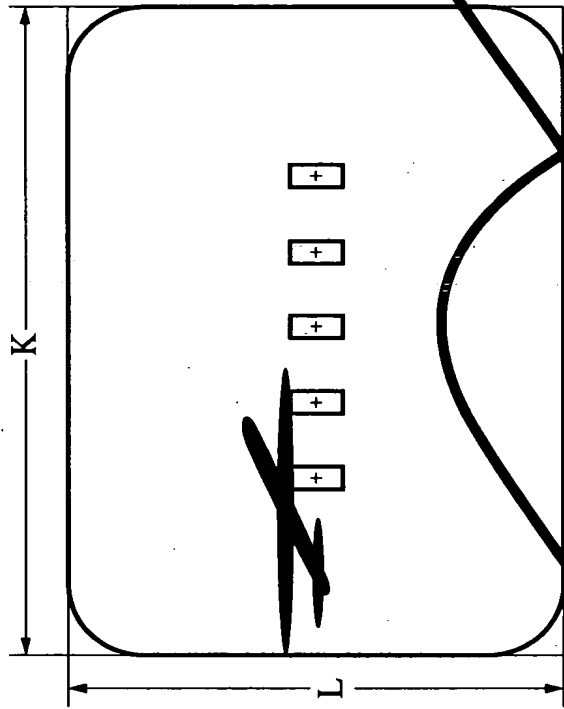
FIG. 17



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

666426-11302205

FIG.18



ENLARGED
(200%)

FIG. 19

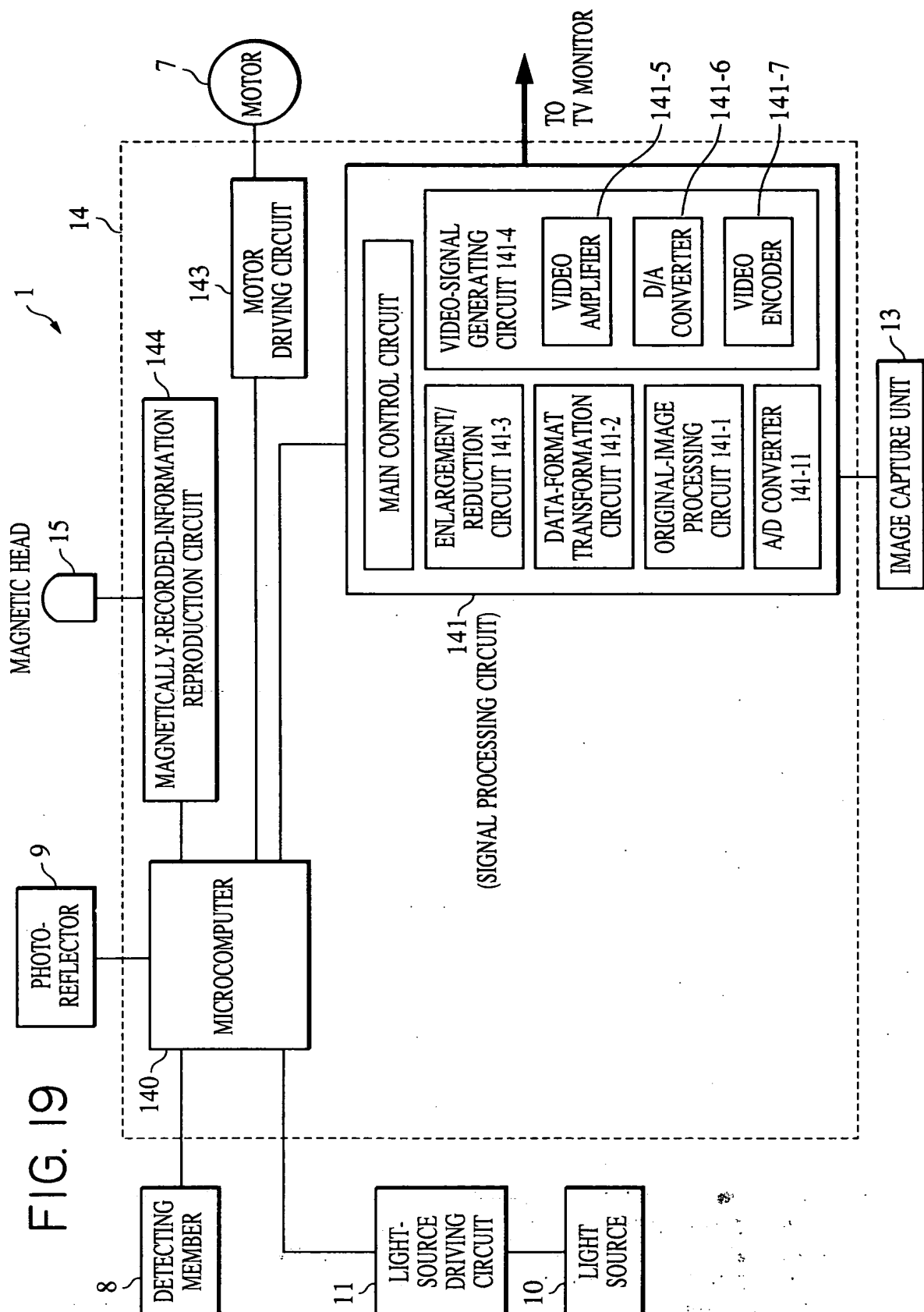
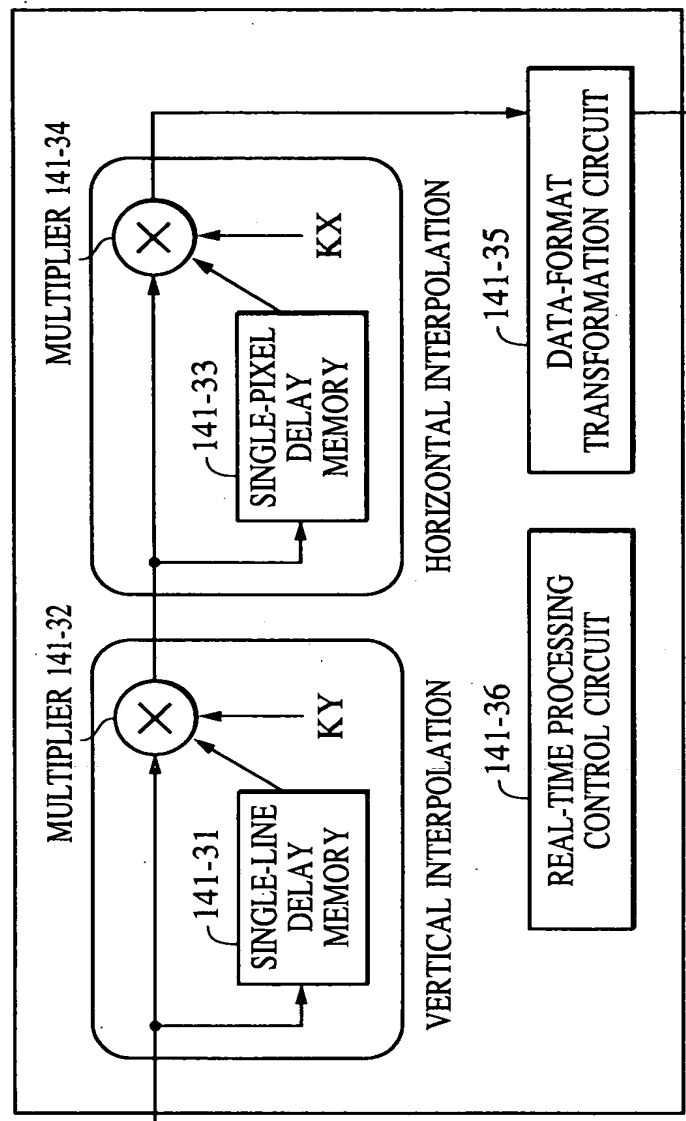
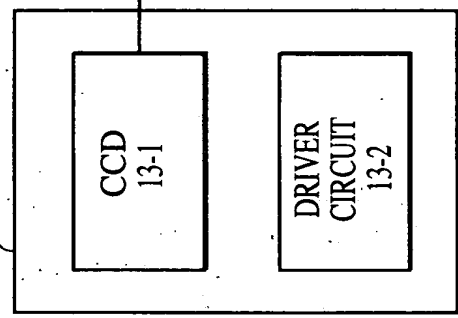


FIG. 20

141 SIGNAL PROCESSING CIRCUIT

IMAGE CAPTURE
13 UNIT

ENLARGEMENT/REDUCTION CIRCUIT 141-3



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 21A

STANDARD SIZE (100%)

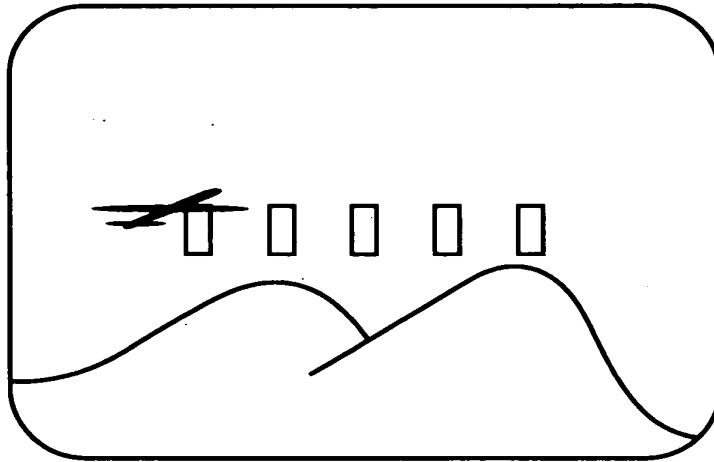


FIG. 21B

ENLARGED (200%)

